



- (e) Differentiate between compilers and interpreters.
- (f) How YACC can be used to generate parser?
- (g) Define DAG.
- (h) Discuss the sub set construction algorithm.
- (i) What is the role of left recursion?
- (j) Discuss the challenges in compiler design.

### Section-B

2. Attempt any five questions from this section. (10×5=50)

- (a) Construct an SLR (1) parsing table for the following grammar

$S \rightarrow A$

$A \rightarrow A, P) (P, P$

$P \rightarrow \{num, num\}$

- (b) Give the algorithm for computing precedence function. Consider the following operator precedence matrix draw precedence graph and compute the precedence function:-

(2)

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	a	(	)	;	\$
A			>	>	>
(	<	<	=	<	
)			>	>	>
;	<	<	>	>	
\$	<	<			

- (c) Define backpatching and semantic rules for Boolean expression. Derive the three address code for the following expression

$P < Q$  or  $R < S$  and  $T < U$

- (d) Generate three address code for the following code

```

switch a+b
{
case 1: x =x+1
case 2: y = y+2
case 3: z=z+3
default :c=c-I
}

```

(3)

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- (e) Construct the **LALR parsing table** for following grammar:

$$S \rightarrow AA$$
$$A \rightarrow aA$$
$$A \rightarrow b$$

- (f) Show that the following grammar

$$S \rightarrow Aa \mid bAc \mid Be \mid bBa$$
$$A \rightarrow d$$
$$B \rightarrow d$$

is LR (1) but not LALR (1).

- (g) What are lexical phase errors, syntactic phase errors and semantic phase errors? Explain with suitable example.
- (h) Describe symbol table and its entries. Also discuss various data structure used for symbol table.

### Section-C

Attempt *any two questions* from this section. (15×2=30)

3. How DAG is different from Syntax Tree? Construct the DAG for the following basic blocks.

(4)

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$a:=b+c$  $b:=b-d$  $c:=c+d$  $e:=b+c$ 

Also explain the key applications of DAG.

4. Consider the following sequence of three address codes:

1. Prod: =0
2. I: =1
3. T1:=4\*I
4. T2:=addr (A)-4
5. T3:=T2 [T1]
6. T4:=addr (B)-4
7. T5:=T4 [T1]
8. T6:=T3\*T5
9. Prod: =Prod+T6

(5)

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10: I=I+1

11: If I<=20 goto (3)

Perform Loop Optimization.

5. Write short notes:

(i) Global Data Flow Analysis

(ii) Loop Unrolling

(iii) Loop Jamming

(6)

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